Patent 4

Attorney's Docket No. 032001-079

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Bradley L. Taylori, et al.

Application No.: 09/960,710

Filed: September 19, 2001

For: Despreader/Correlator Unit For Use

IN RECONFIGURABLE CHIP

Group Art Unit: unknown

Examiner: to be assigned

COPY OF PAPERS ORIGINALLY FILED

## INFORMATION DISCLOSURE STATEMENT TRANSMITTAL LETTER

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Enclosed is an Information Disclosure Statement and accompanying form PTO-1449 for the above-identified patent application.

[X]	No additional	fee for	cubmission	of an	IDS:	is required
IAI	No additional	166 101	2001111221011	UI all	ו טעו	is icquiicu.

- The fee of \$180.00 (126) as set forth in 37 C.F.R. § 1.17(p) is also enclosed.
- A certification under 37 C.F.R. § 1.97(e) is also enclosed.
- A certification under 37 C.F.R. § 1.97(e), and the fee of \$180.00 (126) as set forth in 37 C.F.R. § 1.17(p) are also enclosed.
- [ ] Charge \$\_\_\_\_\_ to Deposit Account No. 02-4800 for the fee due.
- [ ] A check in the amount of \$\_\_\_\_\_ is enclosed for the fee due.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

P.O. Box 1404 Alexandria, Virginia 22313-1404 By: Joseph P. O'Malley

Registration No. 36,226

Redwood Shores, California Office

Tel: (650) 622-2333

Date: December 20, 2001

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#### INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

#### **Non-Patent Literature Documents**

Chameleon Systems, Inc., Product Overview sheet for CS2000 Reconfigurable Communications Processor, © 2000

Chameleon Systems, Inc., Family Product Brief for CS2000 Reconfigurable Communications Processor, Advance Produce Information, © 2000.

Glaskowksky, "Chameleon Crosses CPU, FPGA," Microdesign Resources, June 12, 2000.

Dave Bursky [Editor-in-Chief], "Comm Processor Adjusts For Task At Hand," Electronic Design, May 15, 2000 pg. 66 et seq.

Graham Mostyn, "Benefits of Reconfigurable Processors for Communication System Design," Communication Systems Design, January 2000.

"Reconfigurables hit comms processing," Electronic Engineering Times, May 22, 2000.

Information Disclosure Statement Application No. <u>09/955,913</u> Attorney's Docket No. <u>032001-078</u> Page 2

Crista Souza, "Semiconductors: Chameleon a quick-change artist," Electronic Buyers' News, May 22, 2000.

The documents are being submitted within 3 months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later, therefore no fee or certification is required under 37 C.F.R. § 1.97(b).

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner-initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By:

Joseph P. O'Malley Registration No. 36,226

Redwood Shores, California Office

Tel: (650) 622-2333

P.O. Box 1404 Alexandria, Virginia 22313-1404

Date: December 20, 2001

Substitute for form 1449A/PTO

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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ATTORNEY'S DKT NO.	APPLICATION NO.
032001-079	09/960,710
APPLICANT	
FILING DATE	GROUP
September 19, 2001	unknown

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Initials	item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.									
/	Chameleon Systems, Inc., Product Overview sheet for CS2000 Reconfigurable Communications Processor, © 2000									
V	Chameleon Systems, Inc., Family Product Brief for CS2000 Reconfigurable Communications Processor, Advance Produce Information, © 2000									
1	Glaskowsky, "Chan	neleon Crosses (	CPU, FPGA," Microdesign	Resources, Ju	ine 12, 2	2000.				
/	Dave Bursky [Editor-in-Chief], "Comm Processor Adjusts For Task At Hand," Electronic Design, May 15, 2000 pg. 66 et seq.									
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